

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - III semester - Section A (Scheme-23)

LECTURE HALL : ECE-1

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 4:30pm
MON	PP cr	SSSP ksr	PCV ks	DCD mmsr	LUNCH BREAK	EDC(P)/DCSS(P) kr/mmsr		Student activities/ Remedial classes
TUE	EDC(P)/DCSS(P) kr/mmsr		SSSP ksr	UHV drj		EDC ssb	PCV ks	Student activities/ Remedial classes
WED	EDC ssb	SSSP ksr	DCD mmsr	UHV drj		Python Programming Lab cr		Student activities/ Remedial classes
THU	PCV ks	SSSP ksr	DTI(P) drj			DCD mmsr	EDC ssb	Student activities/ Remedial classes
FRI	DCD mmsr	EDC ssb	PCV ks	SEMINAR as		EDC(P)/DCSS(P) kr/mmsr		Student activities/ Remedial classes
SAT	UHV drj	PCV ks	DCD mmsr	MENTORING		MOOCS kld,srb		Student activities/ Remedial classes

TEA BREAK 10.10 am TO 10.40 am

LUNCH BREAK: 12.20 pm to 1.20 pm

Theory:

- 1 HU201 Probability and Complex Variables(PCV)
- 2 EE211 Universal Human Values(UHV)
- 3 EC201 Signals, Systems and Stochastic Processes(SSSP)
- 4 EC202 Electronic Devices & Circuits (EDC)
- 5 EC203 Digital Circuit Design (DCD)
- 6 SCEC01 Python Programming (PP)

Practicals

- 7 EC204 Electronic Devices & Circuits Lab (EDC(P))
- 8 EC205 Digital Circuits & Signal Simulation Lab (DCSS(P))
- 9 EC206 Design Thinking and Innovation Lab (DTI(P))
- 10 MOOCS Massive Open Online Course(Digital Learning)

Faculty Member:

- | | |
|------|-------------------------|
| ks | Dr.K.Subhashini |
| drj | Dr D. Rajasekhar |
| ksr | Dr K.Suresh Reddy |
| ssb | Dr. S. Saheb Basha |
| mmsr | Dr M. Madhusudhan Reddy |
| cr | Smt C. Rajeswari |

Faculty Member:

- | | |
|---------|--|
| kr | Sri K.Raju |
| mmsr | Dr M. Madhusudhan Reddy |
| drj | Dr D. Rajasekhar |
| kld,srb | Smt K. Lakshmidevi
Sri S. Ramesh Babu |

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - III semester - Section B (Scheme-23)

LECTURE HALL : ECE-2

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 4:30pm
MON	EDC(P)/DCSS(P) ts/gdp		EDC kuk	SSSP ksr	LUNCH BREAK	DCD gdp	MENTORING	Student activities/ Remedial classes
TUE	SSSP ksr	PCV clp	DTI(P) tsb			EDC(P)/DCSS(P) ts/gdp		Student activities/ Remedial classes
WED	EDC kuk	DCD gdp	UHV pcpk	SSSP ksr		PP kr	PCV clp	Student activities/ Remedial classes
THU	EDC(P)/DCSS(P) ts/gdp		PCV clp	SSSP ksr		Python Programming Lab kr		Student activities/ Remedial classes
FRI	DCD gdp	UHV pcpk	EDC kuk	SEMINAR mlm		DCD gdp	PCV clp	Student activities/ Remedial classes
SAT	PCV clp	UHV pcpk	MOOCS gdp,krk			EDC kuk	DCD gdp	Student activities/ Remedial classes

TEA BREAK 10.10 am TO 10.40 am

LUNCH BREAK: 12.20 pm to 1.20 pm

Theory:

- 1 HU201 Probability and Complex Variables(PCV)
 - 2 EE211 Universal Human Values(UHV)
 - 3 EC201 Signals, Systems and Stochastic Processes(SSSP)
 - 4 EC202 Electronic Devices & Circuits (EDC)
 - 5 EC203 Digital Circuit Design (DCD)
 - 6 SCEC01 Python Programming (PP)
- Practicals**
- 7 EC204 Electronic Devices & Circuits Lab (EDC(P))
 - 8 EC205 Digital Circuits & Signal Simulation Lab (DCSS(P))
 - 9 EC206 Design Thinking and Innovation Lab (DTI(P))
 - 10 MOOCS Massive Open Online Course(Digital Learning)

Faculty Member:

- | | |
|------------------------|---|
| clp/vr | Ms. C. Lakshmi Sai Pranathi/ Dr V.Rangaveni |
| pcpk | Dr.P.C.Praveen Kumar |
| ksr | Dr K.Suresh Reddy |
| kur | Smt K. Udaya Kiran |
| gdp | Smt G.Divya Praneetha |
| kr | Sri K. Raju |
| Faculty Member: | |
| kur | Dr T. Swathi |
| gdp | Smt G.Divya Praneetha |
| ts | Sri T. Sarath Babu |
| gdp,krk | Smt .G. Divya Praneetha
Sri K. Ramakrishna |

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

CLASS : B.Tech - III semester - Section C(Scheme-23)

LECTURE HALL : ECE-3

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 4:30pm
MON	PCV clp	SSSP snr	DTI(P) drs		LUNCH BREAK	DCD ap	EDC pspk	Student activities/ Remedial classes
TUE	EDC pspk	DCD ap	SSSP snr	PP drs		UHV mrvv	PCV clp	Student activities/ Remedial classes
WED	DCD ap	SSSP snr	UHV mrvv	PCV clp		EDC(P)/DCSS(P) pspk/ap		Student activities/ Remedial classes
THU	UHV mrvv	DCD ap	SSSP snr	EDC pspk		EDC(P)/DCSS(P) pspk/ap		Student activities/ Remedial classes
FRI	EDC(P)/DCSS(P) pspk/ap		PCV clp	SEMINAR llpk		Python Programming Lab drs		Student activities/ Remedial classes
SAT	MOOCS ts,kr		EDC pspk	DCD ap		PCV clp	MENTORING	Student activities/ Remedial classes

TEA BREAK 10.10 am TO 10.40 am

LUNCH BREAK: 12.20 pm to 1.20 pm

Theory:

- 1 HU201 Probability and Complex Variables(PCV)
- 2 EE211 Universal Human Values(UHV)
- 3 EC201 Signals, Systems and Stochastic Processes(SSSP)
- 4 EC202 Electronic Devices & Circuits (EDC)
- 5 EC203 Digital Circuit Design (DCD)
- 6 SCEC01 Python Programming (PP)

Practicals

- 7 EC204 Electronic Devices & Circuits Lab (EDC(P))
- 8 EC205 Digital Circuits & Signal Simulation Lab (DCSS(P))
- 9 EC206 Design Thinking and Innovation Lab (DTI(P))
- 10 MOOCS Massive Open Online Course(Digital Learning)

Faculty Member:

- | | |
|---------|---|
| clp/gvr | Ms. C. Lakshmi Sai Pranathi/
G.Venkatarami Reddy |
| mrvv | Dr M.V.R.Vittal |
| sn | Dr S. Nagaraja Rao |
| pspk | Sri P. Surya Prakash Reddy |
| ap | Smt A. Parvathi |
| drs | Dr D R Srinivas |

Faculty Member:

- | | |
|-------|-----------------------------|
| pspr | Sri P. Surya Prakash Reddy |
| ap | Smt A. Parvathi |
| drs | Dr D R Srinivas |
| ts,kr | Dr T. Swathi,
Sri K Raju |

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by : PRINCIPAL

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - V semester - Section A(Scheme-20)

LECTURE HALL : ECE-4

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 4:30pm
MON	LCS avjk	AWP llpk	OS(T) ts	MPMC(T) rsb	LUNCH BREAK	OS(T) ts	LDICA(T) ivrr	Student activities/ Remedial classes
TUE	OS ts	LDICA ivrr	AWP(T) llpk	MPMC(T) rsb		LDICA(T) ivrr	AWP(T) llpk	Student activities/ Remedial classes
WED	OE-I krk	MPMC rsb	MPMC(P)/LDICA(P) rsb/ivrr			LCS avjk	EITK jstt	Student activities/ Remedial classes
THU	PCB Design Lab kuk			OS ts		LDICA ivrr	OE-I krk	Student activities/ Remedial classes
FRI	MPMC(P)/LDICA(P) rsb/ivrr		AWP llpk	OS ts		MPMC rsb	OE-I krk	Student activities/ Remedial classes
SAT	MPMC rsb	AWP llpk	LCS avjk	PCBD kuk		LDICA ivrr	EITK jstt	Student activities/ Remedial classes

TEA BREAK 10.10 am TO 10.40am

LUNCH BREAK: 12.20pm to 1.20pm

Theory:

- 1 EC301 Microprocessors and MicroControllers (MPMC)
- 2 EC302 Linear and Digital IC Applications (LDICA)
- 3 EC303 Antennas and Wave Propagation(AWP)
- 4 EE317 Linear Control Systems(LCS)
- 5 EC305 Professional Elective Course –I(PEC-1) (Operating Systems(OS))-
- 6 Open Elective Course -I(OEC-I)
- 7 MC Essence of Indian Traditional Knowledge (EITK)

Practicals

- 8 EC307 Microprocessors and Micro Controllers Lab (MPMC Lab)
- 9 EC308 Linear and Digital IC Applications Lab (LDICA Lab)
- 10 SCEC03 PCB Design Lab (PCBD Lab)

Faculty Member:

- | | |
|------|-------------------------------|
| rsb | Dr R. Sudheer Babu |
| ivrr | Sri I.Venkata Rameswara Reddy |
| llpk | Dr L.Lakshmi Prasanna Kumar |
| avkr | Sri A.Vijay Kumar Reddy |
| ts | Dr. T.Swathi |
| jstt | Dr J.S.T.Thillagam |
| rsb | Dr R. Sudheer Babu |
| ivrr | Sri I.Venkata Rameswara Reddy |
| kuk | Smt K.Udaya Kiran |

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : **B.Tech - V semester - Section B(Scheme-20)**

LECTURE HALL **ECE - 5**

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 4:30pm
MON	MPMC(P)/LDICA(P) gr/dro		OS(T) dro	LDICA(T) sv	LUNCH BREAK	AWP(T) jstt	MPMC(T) gr	Student activities/ Remedial classes
TUE	MPMC gr	LCS avjk	OS(T) dro	MPMC(T) gr		AWP(T) jstt	LDICA(T) sv	Student activities/ Remedial classes
WED	OE-I wy	AWP jstt	LCS avjk	LDICA sv		MPMC(P)/LDICA(P) gr/dro		Student activities/ Remedial classes
THU	LDICA sv	OS dro	AWP jstt	MPMC gr		LCS avjk	OE-I wy	Student activities/ Remedial classes
FRI	PCB Design Lab gak			OS dro		EITK kld	OE-I wy	Student activities/ Remedial classes
SAT	OS dro	AWP jstt	EITK kld	PCB gak		MPMC gr	LDICA sv	Student activities/ Remedial classes

TEA BREAK 10.10 am TO 10.40am

LUNCH BREAK: 12.20pm to 1.20pm

Theory:

1	EC301	Microprocessors and Micro Controllers (MPMC)	gr
2	EC302	Linear and Digital IC Applications (LDICA)	sv
3	EC303	Antennas and Wave Propagation(AWP)	jstt
4	EE317	Linear Control Systems(LCS)	avkr
5	EC305	Professional Elective Course –I(PEC-1) (Operating Systems(OS))- Open Elective Course -I(OEC-I)	dro
7	MC	Essence of Indian Traditional Knowledge (EITK)	kld

Practicals

8	EC307	Microprocessors and Micro Controllers Lab (MPMC Lab)	gr
9	EC308	Linear and Digital IC Applications Lab (LDICA Lab)	dro
10	SCEC03	PCB Design Lab (PCB Lab)	gak

Faculty Member:

Dr G.Ramesh
Dr S.Vsyhali
Dr J.S.T.Thillagam
Sri A.Vijay Kumar Reddy
Smt D.Rohini
Smt K. Lakshmidevi
Dr G.Ramesh
Smt D.Rohini
Dr G.Amjad Khan

Prepared by : **B. Siva Reddy**

Verified by: **HOD (E.C.E.)**

Approved by : **PRINCIPAL**

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - V semester - Section C(Scheme-2)

LECTURE HALL :ECE-6

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm -1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 4:30pm
MON	LCS msr	EITK tsb	OS(T) gdp	LDICA(T) cr	LUNCH BREAK	AWP(T) as	MPMC(T) smsd	Student activities/ Remedial classes
TUE	LDICA cr	MPMC smsd	AWP(T) as	OS(T) gdp		LDICA(T) cr	MPMC(T) smsd	Student activities/ Remedial classes
WED	OE-I	PCB Design Lab llpk				AWP as	OS gdp	Student activities/ Remedial classes
THU	MPMC(P)/LDICA(P) smsd/cr		OS gdp	MPMC smsd		PCB llpk	OE-I	Student activities/ Remedial classes
FRI	AWP as	LCS msr	MPMC(P)/LDICA(P) smsd/cr			LDICA cr	OE-I	Student activities/ Remedial classes
SAT	OS gdp	MPMC smsd	LDICA cr	AWP as		LCS msr	EITK tsb	Student activities/ Remedial classes

TEA BREAK 10.10 am TO 10.40am

LUNCH BREAK: 12.20pm to 1.20pm

Theory:

1	EC30 Microprocessors and Micro Controllers (MPMC)	smsd
2	EC30 Linear and Digital IC Applications (LDICA)	cr
3	EC30 Antennas and Wave Propagation(AW)	as
4	EE31 Linear Control Systems(LCS)	msr
5	EC30 Professional Elective Course –I(PEC-1) (Operating Sy	gdp
6	Open Elective Course -I(OEC-I)-IOT	
7	MC Essence of Indian Traditional Knowledge (EITK)	tsb

Practicals

8	EC30 Microprocessors and Micro Controllers Lab (MPMC L	smsd
9	EC30 Linear and Digital IC Applications Lab (LDICA Lab)	cr
10	CEC(PCB Design Lab(PCB Lab)	llpk

Faculty Member:

Dr S.M.Shamsheer Daula
Smt.C.Rajeswari
Dr. A. Sowjanya
Sri M. Sreenivasa Reddy
Smt G.Divya Praneetha
Sri. T. Sharath Babu
Dr S.M.Shamsheer Daula
Smt.C.Rajeswari
Dr L.Lakshmi Prasanna Kumar

GPREC-F / CFD / TIME TABLE

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - VII semester - Section A(Scheme-20)

LECTURE HALL : ECE - 7

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 4:30pm
MON	CMC pcpk	IP pak	DES wy	CN mlm	LUNCH BREAK	DS(P) Lab pcpk		Student activities/ Remedial classes
TUE	IP pak	FPGA & ASIC gvrs	DS(P) Lab pcpk			UHV-2 kuk	CN mlm	Student activities/ Remedial classes
WED	PW jstt		UHV-2 kuk	DES wy		FPGA & ASIC gvrs	IP pak	Student activities/ Remedial classes
THU	PW jstt		IP pak	FPGA & ASIC gvrs		DES wy	CMC pcpk	Student activities/ Remedial classes
FRI	CN mlm	CMC pcpk	FPGA & ASIC gvrs	UHV-2 kuk		PW jstt		Student activities/ Remedial classes
SAT	DES wy	CN mlm	CMC pcpk	Mentoring		ITCA	ITCA	Student activities/ Remedial classes

TEA BREAK 10.10 am TO 10.40am

LUNCH BREAK: 12.20pm to 1.20pm

NOTE:FRS Attendance must be taken every day first hour

Theory:

1	CMC	Cellular and Mobile Communications (Professional Elective-III)	---
2	CN	Computer Networks(Professional Elective-IV)	---
3	FPGA & ASIC	FPGA and ASIC Architectures and Applications(Professional Elective-V)	---
4	DES	Distributed Embedded Systems(Open Elective-III)	---
5	IP	Image Processing(Open Elective-IV)	---
6	UHV2	Universal Human Values-I,-2	---

Practicals

7	DSL	Data Science Lab(DSL)	---
8	PW	Project Work(PW)	---

Faculty Member:

pcpk	Dr P.C. Praveen Kumar
mlm	Smt M. Lalithameghana
gvr	Dr G.V.R Sagar
wy	Smt. W.Yasmeen
pak	Dr P. Abdul Khayum
kuk	Smt K. Udaya Kiran
pcpk	Dr P.C. Praveen Kumar
jstt	Dr J.S.T.Thillagam

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - VII semester - Section B(Scheme-20)

LECTURE HALL : ECE - 8

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 4:30pm
MON	DS(P) Lab drs		FPGA &ASIC srb	CN ap	LUNCH BREAK	CMC gvrs	IP rsb	Student activities/ Remedial classes
TUE	IP rsb	UHV-2 tsb	CMC gvrs	DES smsd		DS(P) Lab drs		Student activities/ Remedial classes
WED	DES smsd	CMC gvrs	PW srb			FPGA &ASIC srb	DES smsd	Student activities/ Remedial classes
THU	FPGA &ASIC srb	IP rsb	CN ap	UHV-2 tsb		PW srb		Student activities/ Remedial classes
FRI	CMC gvrs	DES smsd	IP rsb	CN ap		UHV-2 tsb	FPGA &ASIC srb	Student activities/ Remedial classes
SAT	CN ap	Mentoring	PW srb			ITCA	ITCA	Student activities/ Remedial classes

TEA BREAK 10.10 am TO 10.40am

LUNCH BREAK: 12.20pm to 1.20pm

NOTE:FRS Attendance must be taken every day first hour

Theory:				Faculty Member:	
1	CMC	Cellular and Mobile Communications (Professional Elective-III)	---	gvr	Dr G.V.R Sagar
2	CN	Computer Networks(Professional Elective-IV)	---	ap	Smt A.Parvathi
3	FPGA & ASIC	FPGA and ASIC Architectures and Applications(Professional Elective-V)	---	srb	Sri S. Ramesh Babu
4	DES	Distributed Embedded Systems(Open Elective-III)	---	smsd	Dr S.M.Shamsheer Daula
5	IP	Image Processing(Open Elective-IV)	---	rsb	Dr R. Sudheer Babu
6	UHV2	Universal Human Values:-2	---	tsb	Sri T. Sarath Babu
Practicals					
7	DSL	Data Science Lab(DSL)	---	drs	Dr D.R.Srinivas
8	PW	Project Work(PW)	---	srb	Sri S. Ramesh Babu

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

GPREC-F / CFD / TIME TABLE

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - VII semester - Section C(Scheme-20)

LECTURE HALL ECE - 9

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 4:30pm
MON	IP drj	CMC mvrv	DS(P) Lab mvrv		LUNCH BREAK	CN dvsb	DES krk	Student activities/ Remedial classes
TUE	DS(P) Lab mvrv		CMC mvrv	DES krk		IP drj	FPGA & ASIC dro	Student activities/ Remedial classes
WED	CN dvsb	UHV-2 kr	FPGA & ASIC dro	DES krk		PW ivrr		Student activities/ Remedial classes
THU	UHV-2 kr	IP drj	PW ivrr			FPGA & ASIC dro	CN dvsb	Student activities/ Remedial classes
FRI	FPGA & ASIC dro	CMC mvrv	IP drj	CN dvsb		DES krk	CMC mvrv	Student activities/ Remedial classes
SAT	PW ivrr		UHV-2 kr	Mentoring		ITCA	ITCA	Student activities/ Remedial classes

TEA BREAK 10.10 am TO 10.40am

LUNCH BREAK: 12.20pm to 1.20pm

NOTE:FRS Attendance must be taken every day first hour

Theory:

1	CMC	Cellular and Mobile Communications (Professional Elective-III)	---
2	CN	Computer Networks(Professional Elective-IV)	---
3	FPGA & ASIC	FPGA and ASIC Architectures and Applications(Professional Elective-V)	---
4	DES	Distributed Embedded Systems(Open Elective-III)	---
5	IP	Image Processing(Open Elective-IV)	---
6	UHV2	Universal Human Values-2	---

Practicals

7	DSL	Data Science Lab(DSL)	---
8	PW	Project Work(PW)	---

Faculty Member:

mvrv	Dr M.V.R Vittal
dvsb	Dr D. V. Srihari Babu
dro	Smt D. Rohini
krk	Sri K. Ramakrishna
drj	Dr D. Rajasekhar
krk	Sri K. Raju
mvrv	Dr M.V.R Vittal
ivrr	Sri I. Venkata Rameswar Reddy

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - V semester - Minor Degree Course in IOT(Scheme-20)

Lecture Hall :ECE-4

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 3.50pm
MON					LUNCH BREAK			
TUE								
WED								IAP gr
THU								IAP gr
FRI								IAP gr
SAT								

1 MIT02

Theory:
IoT Architecture and Protocols (IAP)

gr

Faculty Member:
Dr G. Ramesh

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by : PRINCIPAL

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TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - VII semester - Minor Degree Course in IOT(Scheme-20)

Lecture Hall :ECE-7

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 3.50pm
MON					LUNCH BREAK			
TUE								
WED								IIT wy
THU								IIT wy
FRI								IIT wy
SAT								

1 MIT04

Theory:
Industrial Internet of Things(IIT)

wy

Faculty Member:
Smt W. Yasmeeen

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - V semester - Minor Degree Course in INDUSTRIAL ROBOTICS(Scheme-20)

Lecture Hall : ECE-4

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 3.50pm
MON					LUNCH BREAK			
TUE								
WED								PLC mlm
THU								PLC mlm
FRI								PLC mlm
SAT								

Theory:

1 MIR02 Programmable Logic Controllers(PLC)

mlm

Faculty Member:

Smt M. Lalitha Meghana

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by : PRINCIPAL

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 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - VII semester - Minor Degree Course in INDUSTRIAL ROBOTICS(Scheme-20) Lecture Hall :ECE

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 3.50pm
MON					LUNCH BREAK			
TUE								
WED								RPA kr
THU								RPA kr
FRI								RPA kr
SAT								

1 MIR04 Theory:
 Robotic Process Automation(RPA)

Faculty Member:
 kr Sri K. Ramakrishna

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - V semester - Honors Degree Course in ECE(Scheme-20)

Lecture Hall :ECE-6

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 3.50pm
MON					LUNCH BREAK			
TUE								
WED								ASIC mmsr
THU								ASIC mmsr
FRI								ASIC mmsr
SAT								

1 HEC02

Theory:

Application Specific Integrated Circuits (ASIC)

mmsr

Faculty Member:

Dr M. Madhusudhan Reddy

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - VII semester - Honors Degree Course in ECE(Scheme-20)

Lecture Hall :ECE-9

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10am	10.40 am - 11.30 pm	11.30 pm - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm to 3.00pm	3.00pm to 3.50pm
MON					LUNCH BREAK			
TUE								
WED								ENP bsr
THU								ENP bsr
FRI								ENP bsr
SAT								

1 HEC04

Theory:
Embedded Networks and Protocols(ENP)

bsr

Faculty Member:
Sri B. Siva Reddy

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL